



H-Form ECE 462

| A Brief Course Description | | | |
|----------------------------|---|-----------------|-------------|
| College | Engineering | | |
| Department/ Program | Electrical Engineering – Electronics Engineering Program | | |
| Course Name | Computer Systems Architecture | | |
| Course Code | ECE 462 | | |
| Year / Level | 4/8 | | |
| Credit Hours | 4 | | |
| Contact Hours | Lecture: 3 | Lab/Tutorial: 2 | Training: 0 |
| Language | English | | |
| Track | <input type="checkbox"/> University <input type="checkbox"/> College <input type="checkbox"/> Department <input checked="" type="checkbox"/> Program | | |
| | <input type="checkbox"/> Required <input checked="" type="checkbox"/> Elective | | |
| Pre-requisites Course | ECE 342 | | |
| Co-Requests | - | | |
| Course Description | Computer system organization and design, implementation of CPU datapath and control, instruction set design, memory hierarchy (caches, main memory, virtual memory) organization and management, input/output subsystems (bus structures, interrupts, DMA), performance evaluation, pipelined processors. | | |